



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Park et al.
Assignee: Samsung Electronics Co. Ltd.
Title: Semiconductor Memory Device And Method Of Manufacturing For Preventing Bit Line Oxidation
Serial No.: 09/008,525 Filing Date: June 16, 1998
Examiner: Nguyen, Cuong Quang Group Art Unit: 2811
Docket No.: AB-778 US

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San Jose, California
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COMMISSIONER FOR PATENTS
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RESPONSE TO RESTRICTION

Dear Sir:

In response to the Restriction Requirement in the above-identified application having a mailing date of October 9, 2002, Applicants hereby elect Claims 1-35 with traverse.

EXPRESS MAIL LABEL NO:

EV 160 614 905 US

Respectfully submitted,

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